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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re application of:
W. SLEMMER

Serial No.

Filed: HEREWITH

Eor: DIRECT CURRENT SUMBANDGAP VOLTAGE

COMPARATOR

Group Art Unit

Examiner:

Attorney Dkt. No.: 93-C-07C2

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

I certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on September 15, 1997.

By:_____

PRELIMINARY AMENDMENT

Box: FWC

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

Prior to examination of the above-identified continuation application, please amend the application as follows. A check for the required fees is enclosed.

IN THE CLAIMS:

Please cancel claims 1, 4-14, 17-26 and 30.

Please amend the claims as follows:

- 27. (twice amended) A direct current sum bandgap voltage comparator comprising:
- a power supply having a predetermined threshold voltage level which defines the minimum acceptable voltage level of the power supply;
 - a summing node;
- a plurality of current sources connected to the summing node and directly connected to a power supply voltage, each current source further comprising at least one <u>field effect</u> transistor and at least one <u>bipolar junction transistor</u> and each current

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